

Description

System and method for establishing an xDSL data transfer link

- 5 The invention relates to a system and a method for establishing a data transfer link between an xDSL user modem and a corresponding xDSL modem within a central office.

Fig. 1 shows a conventional xDSL system with several xDSL user modems, each connected via a telephone line to a corresponding xDSL modem within the central office. In a normal operation, the conventional xDSL modem within the central office is operating continuously and tries to establish a data transfer link with the corresponding remote user xDSL modem connected via a telephone line. When the user xDSL modem is ready for data transfer, a data link is established by means of a start-up procedure. A central office comprises a very large number of xDSL modems connecting the users of the telephone network. The disadvantage of the conventional xDSL system as shown in Fig. 1 is that the xDSL modems within the central office continuously try to establish a connection with the corresponding user xDSL modem and therefore consume much power. An xDSL modem within a central office consumes up to 1,5 Watt of power. Since the number of xDSL modems within a central office can be very large, the power consumption of all xDSL modems is significant.

Accordingly, it is the object of the present invention to provide a method and a system for data transfer between an xDSL user modem and a corresponding xDSL modem within a central office which consume a minimum amount of power.

This object is achieved by a method for establishing a data transfer link between an xDSL user modem and a corresponding xDSL modem within a central office having the features of

claim 1 and by an xDSL data transfer system having the features of patent claim 8.

The present invention provides a method for establishing a data transfer link between an xDSL user modem and the corresponding xDSL modem within the central office comprising the following steps:

generating a wake-up bit pattern identifying the user modem, pulse length modulating the generated wake-up bit pattern to generate a pulse length modulated wake-up signal, transmitting the pulse length modulated wake-up signal from the user xDSL modem via a data transfer medium to the corresponding xDSL modem within the central office, demodulating the transmitted wake-up signal by the xDSL modem within the central office, comparing the demodulated wake-up signal with a stored wake-up bit pattern for the detection of a transmission of the wake-up bit pattern, generating a wake-up command signal for switching the xDSL modem within the central office from a sleep mode to an operation mode for data transfer, when the wake-up bit pattern is detected.

In the sleep mode, the power consumption of the xDSL modem within the central office is low, thus minimizing the overall power consumption within the central office.

In a preferred embodiment of the method according to the present invention, the xDSL modem within the central office commences a start-up procedure, when it is switched to the operation mode.

In a further embodiment, the wake-up signal is transmitted periodically by the xDSL user modem.

In a further embodiment, the xDSL modem within the central office is switched from the operation mode to the sleep mode when the data transfer is finished.

- 5 In a further preferred embodiment, a detection counter is incremented when the wake-up bit pattern is detected.

In a further preferred embodiment, a wake-up command signal is generated when the detection counter reaches a
10 predetermined threshold value.

In a preferred embodiment, the threshold value for the detection counter is adjusted.

- 15 The invention provides further an xDSL data transfer system for data transfer comprising at least one xDSL user modem connected via a data transfer medium to a corresponding xDSL modem within the central office,
wherein the xDSL user modem generates a wake-up signal for
20 switching the corresponding the xDSL modem within the central office from a sleep mode to an operation mode.

In a preferred embodiment, the wake-up signal is pulse-length modulated.

- 25 In a preferred further embodiment, the xDSL user modem comprises
generating means for generating a wake-up pattern identifying the xDSL user modem, and
30 modulating means for pulse length modulation of the wake-up pattern to generate a pulse length modulation wake-up signal, wherein the pulse length modulated wake-up signal has a spectrum within the xDSL upstream frequency band.

- 35 In a preferred embodiment of the xDSL data transfer system according to the present invention, the generated wake-up bit pattern comprises 16 bits.

In a further embodiment, each bit of the wake-up bit pattern determines the duration of a pulse in the pulse length modulated signal.

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In a still further embodiment, the level of the wake-up signal is less than a predetermined maximum PSD-signal level.

In a further embodiment of the xDSL data transfer system according to the present invention, the xDSL modem within the central office comprises demodulating means for demodulating the received analog pulse length modulated signal, storing means for storing a wake-up bit pattern which identifies a user modem, detecting means for detecting the demodulated wake-up bit pattern by comparing the demodulated received signal with the stored wake-up bit pattern, wake-up command generating means for generating a wake-up command to switch the xDSL modem from the sleep mode to the operation mode, when the received wake-up bit pattern is identical with the stored wake-up bit pattern.

In a further embodiment of the xDSL data transfer system according to the present invention, the demodulating means of the xDSL modem comprises a gain sequencer for amplifying the received analog signal with an adjustable gain, rectifying means for rectifying the amplified signal, a low-pass filter for filtering the rectified signal, and a comparator for comparing the filtered signal with an adjustable threshold generating an asynchronous pulse train supplied to the detecting means.

In a preferred embodiment of the xDSL data transfer system according to the present invention, the detecting means comprises

a synchronization means for synchronizing the asynchronous pulse train with an internal clock signal,
a pulse length detecting circuit for detecting the pulse length of each pulse in the synchronized pulse train,
5 a bit pattern generating means for generating a bit pattern depending on the detected pulse length,
a register for temporarily storing the received bit pattern,
a comparator which compares the received bit pattern with the stored wake-up bit pattern and which increments a counter
10 when the received bit pattern and the stored wake-up bit pattern is identical.

In a preferred embodiment of the xDSL system according to the present invention, the wake-up command generating means
15 generates the wake-up command when the counter reaches a predetermined threshold value.

In a preferred embodiment of the xDSL system according to the present invention the xDSL modems are VDSL modems.
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In a preferred embodiment of the xDSL data transfer system, the data transfer medium is a telephone line.

The invention provides further an xDSL user modem comprising
25 a generating means for generating a wake-up pattern identifying the xDSL user modem, and
a modulating means for pulse length modulation of the wake-up bit pattern to generate the pulse length modulated wake-up signal,
30 wherein the pulse length modulated wake-up signal has a spectrum within the xDSL upstream frequency band.

The invention provides further an xDSL central office modem within a central office comprising
35 demodulating means for the demodulation of a received analog pulse length modulated signal,

storing means for storing a wake-up bit pattern which identifies the user modem connected to the xDSL office modem via a data transfer medium,
detecting means for detecting a demodulated wake-up bit
5 pattern by comparing the demodulated received signal with the stored wake-up bit pattern,
a wake-up command generating means for generating a wake-up command to switch the xDSL modem from the sleep mode to the operation mode when the received wake-up bit pattern is
10 identical with the stored wake-up pattern.

In the following description, a preferred embodiment of a method for establishing a data transfer link and an xDSL data transfer system for data transfer according to the present
15 invention is described with reference to the drawings.

Fig. 1 shows an xDSL data transfer system for data transfer according to the state of the art;

20 Fig. 2 shows an xDSL data transfer system for data transfer according to the present invention;

Fig. 3 shows a block diagram of additional units within an xDSL user modem according to the present invention;

25 Fig. 4 shows a block diagram of an xDSL modem within the central office according to the present invention;

Fig. 5 shows demodulating means of an xDSL modem within the
30 central office according to the present invention;

Fig. 6 shows a block diagram of detecting means of the xDSL modem within the central office according the present
invention;

35 Fig. 7 shows a timing diagram of the pulse length modulated wake-up signal according to the present invention;

Fig. 8a, b show a flow diagram of the generation of a wake-up command signal by an xDSL modem within the central office according to the present invention.

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The xDSL data transfer system 1 for data transfer according to the present invention is shown in Fig. 2. The xDSL data transfer system comprises N data sources 2-1 to 2-N, each generating a data upstream signal. The data upstream signal is supplied via signal lines 3-1 to 3-N to the corresponding xDSL user modem 4-1 to 4-N. The xDSL user modem 4 consists of a conventional xDSL modem 5 which is connected on its output side via a line 6 to a wake-up signal generating unit 7 which generates a wake-up signal. The xDSL user modem 4 is connected via a physical medium, e.g. a twisted telephone line, to a corresponding xDSL modem 9 within a central office 10. Each xDSL modem 9 of the central office 10 comprises a conventional xDSL modem 11 and a wake-up signal detection unit 12 for detecting a wake-up signal received via the telephone line 8. The wake-up signal detection unit 12 is connected to the telephone 8 via line 13 and sends a wake-up command signal via a control line 14 to the xDSL modem 11 to switch the xDSL modem from a sleep mode to an operation mode for data reception.

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The wake-up signal generating unit 7 of the xDSL user modem 4 generates a wake-up signal via the telephone line 8 to its corresponding xDSL modem 9 within the central office 10. The transmitted wake-up signal is detected by the wake-up signal detection unit 12 so that the xDSL modem 11 is switched from a sleep mode to an operation mode for data transfer. Each user modem 4 generates a wake-up signal which is unique to the user modem 4 so that the wake-up signal detection unit 12 is insensitive to far end cross-talk (FEXT) between different telephone lines 8. The unique wake-up signal for each user modem 4 guarantees that erroneous wake-ups of other xDSL-modems as a consequence of far end cross-talk (FEXT) are

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prevented. Accordingly, the different telephone lines 8-1 to 8-N can be arranged in a common telephone cable 15.

When an xDSL user modem 4 is inactive, the corresponding xDSL modem 9 within the central office 10 is in a sleep mode. Since a data source 2 like a personal computer 2 normally does not send a data upstream signal to the central office for the most part of the day, the corresponding xDSL modem 9 within the central office 10 is in the sleep mode for most of the time. In the sleep mode, the xDSL modem 9 consumes only a minimum necessary amount of electrical power. The xDSL modem 9 is switched to the power-consuming operation mode only when the wake-up signal from the corresponding xDSL user modem 4 is detected. When the user turns on the power of the user modem 4 and wishes to establish a data connection, the user modem 4 generates a wake-up signal and sends it via the telephone line 8 to the corresponding xDSL modem 9 in the central office. The wake-up signal detection unit 12 detects a received wake-up signal and switches the xDSL modem 9 to an operation mode. The xDSL modem 9 then commences a start-up procedure. When the user ends the data connection, the xDSL modem 9 within the central office 10 is switched back to the sleep mode.

Fig. 3 shows a wake-up signal generating unit 7 within an xDSL user modem 4 according to the present invention. The wake-up signal generating unit 7 comprises a wake-up pattern generating means 16 for generating a wake-up bit pattern identifying the xDSL user modem 4. The generated wake-up bit pattern is supplied via a line 17 to a modulation means 18. The modulation means 18 performs a pulse length modulation of the upstream data signal supplied from the data source 2 via data line 6. The upstream data signal is pulse length modulated with the wake-up bit pattern to generate a pulse length modulated wake-up signal which is transmitted via line 8 to the corresponding xDSL modem 9 within the central office 10. The pulse length modulated wake-up signal has a frequency

range within the xDSL upstream frequency band. For a VDSL application, the upstream frequency band is between 3,5 MHz and 5 MHz. The generated wake-up signal can pass through passive magnetic elements such as transformers, and does not get blocked before it reaches the wake-up detection unit 12 within the xDSL modem 9 in the central office 10.

In a preferred embodiment, the wake-up bit pattern generating means 16 generates a wake-up bit pattern which comprises 16 bits. This comparatively short bit pattern ensures that the wake-up process is completed within a relatively short time, e.g. within 100 milliseconds.

Fig. 7 shows an example for a wake-up signal generated by the wake-up generating means 7 of a user xDSL modem 4. The wake-up signal has a flat frequency spectrum in the xDSL upstream frequency band modulated by a duty cycle modulation. A sequence of N bits each of a duration of d microseconds is transmitted periodically by the user modem 4 until it is detected by the wake-up detection unit 12 within the xDSL modem 9 of the central office 10. Each bit of the wake-up bit pattern is represented by a pulse of d_{on} microseconds on and d_{off} microseconds off, where $d_{on} < d_{off}$, if the bit is logical low, and $d_{on} > d_{off}$, if the bit is logical high. In a preferred embodiment, the wake-up bit pattern comprises 16 bits, duration d is approximately 100 microseconds, wherein $d_{on} = 0,25 d$, when the bit is logical low, and $d_{on} = 0,75 d$, when the bit is logically high. Fig. 7 shows an example for a wake-up signal during one period of the wake-up signal. Fig. 7 shows only the signal envelope. The signal itself is a random noise signal whose spectrum coincides with the spectrum of a standard xDSL upstream signal.

Fig. 4 shows a block diagram of the wake-up signal detection unit 12 within an xDSL modem 9 of the central office 10. The wake-up signal detection unit 12 comprises demodulating means 19 for demodulating the received analog pulse length

modulated signal. The demodulating means 19 are connected via a line 20 to comparing means 21 which compare the demodulated received bit pattern with the wake-up bit pattern stored within storing means 22. The storing means 22 store a wake-up
5 bit pattern which identifies the corresponding user modem 4. The storing means 22 are connected to the comparing means 21 via a line 23. If the comparator 22 detects that the demodulated bit pattern is identical with the stored wake-up bit pattern, it sends a detection signal via a line 24 to a
10 wake-up command generating means 25 which generates the wake-up command and transmits the wake-up command via a control line 14 to the corresponding modem 11. The wake-up command switches the xDSL modem 9 from the sleep mode to the operation mode. The wake-up signal detection unit 12 can be
15 implemented by a simple circuitry that does not require large power. Accordingly, the power consumption of the wake-up signal detection unit 12 is only a small percentage of the power consumed by the complete xDSL modem 9. The wake-up signal detection unit is insensitive to the absolute power
20 level of the received wake-up signal in order to accommodate all line distances. The wake-up signal detection unit 12 is provided for detecting a bit pattern, not a signal energy.

Fig. 5 shows a preferred embodiment of the demodulating means
25 19 within the wake-up detection unit 12 according to the present invention. The demodulating means 19 comprises a gain sequencer 26 for amplifying the received analog signal with an adjustable gain, the rectifying circuit 27 rectifying the amplified signal, a low-pass filter 28 for filtering the
30 rectified signal and a comparator 29 for comparing the filtered signal with an adjustable threshold generating an asynchronous pulse train supplied to the detecting means 21. The gain sequencer 26 amplifies the incoming wake-up signal and comprises an operation amplifier 30, resistors 31a, 31b
35 and adjustable resistors 32a, 32b. The gain sequencer 26 amplifies the incoming wake-up signal with an increasing gain until the wake-up signal is detected by the wake-up detection

means. The rectifying means 27 comprising an operational control amplifier 33 which controls switches 34a, 34b supplies a rectified signal to the following low-pass filter 28. The low-pass filter 28 shown in Fig. 5 removes the high frequency noise and tracks the signal envelope of the received wake-up signal. The low-pass filter 28 further removes residual DC signal components. The low-pass filter 28 is an active filter comprising an operational amplifier 35, resistors 36, 37, 38 and capacitors 39, 40. The output of the operational amplifier 35 is connected via line 41 to comparing means 29 including a comparator 42, a resistor 43a and a capacitor 44a.

The output of the comparator 42 is connected via line 20 to the bit pattern comparing means 21 comparing the output bit pattern of the comparator 42 with the stored wake-up bit pattern. The bit pattern comparing means 21 generates a detecting signal with the received bit pattern output by a comparator 42 which is identical with the wake-up bit pattern stored in the storage means 22 and sends the detecting signal to the wake-up command generating means 25.

Fig. 6 shows a preferred embodiment of the bit pattern detecting means 21. The bit pattern detecting means 21 comprises a synchronization circuit 43 for synchronizing the asynchronous pulse train received from the demodulating means 19. The received pulse train or bit pattern is synchronized with an internal clock signal. The detecting means 21 further comprises a pulse length detecting circuit 44 connected to the synchronization circuit 43 via line 45. The output of the pulse length detecting circuit 44 is connected on its output side via line 46 to a register 47 for temporarily storing the received bit pattern. A bit comparator 48 connected to the register 47 via a line 49 compares the received bit pattern stored in the register 47 with the wake-up bit pattern stored in the memory 22 and increments a counter 51 via a control line 50, when the received bit pattern and the stored bit

pattern is identical. The wake-up command generating means 25 generates a wake-up command, when the counter 51 reaches an adjustable threshold value.

5 Fig. 8a shows a flow diagram of a method for establishing a data transfer link between the xDSL user modem 4 and the corresponding xDSL modem 9 in the central office 10 according to the present invention.

10 In a step S1, a wake-up bit pattern identifying the xDSL user modem 4 is generated by a bit pattern generating circuit 16.

In a step S2, the generated wake-up bit pattern is used for pulse length modulating an upstream data signal to generate a
15 pulse length modulated wake-up signal by means of a modulating circuit 18.

The pulse length modulated wake-up signal is transmitted from the user xDSL modem 4 via the telephone line 8 to the xDSL
20 modem 9 within the central office 10 in a step S3.

In a further step S4, the transmitted wake-up signal is demodulated by demodulating means 19 within the wake-up signal detection unit 12 of the xDSL modem 9.

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In a step S5, the demodulated wake-up signal is compared with a wake-up bit pattern stored in the register 22.

In step S6, a wake-up command signal is generated when the
30 received bit pattern is identical with the stored wake-up bit pattern, and the modem 11 is switched from a sleep mode to an operation mode to start a start-up procedure.

After the modem 11 has been switched into the operation mode,
35 the xDSL modem 9 commences a start-up procedure to establish a data link with the corresponding xDSL user modem 4. After the start-up procedure, the normal data transfer is

performed. When the data transfer is finished, the xDSL modem 9 within the central office 10 is switched from the operation mode to the sleep mode.

- 5 Fig. 8b shows a preferred embodiment of step S6 within the main routine according to the present invention as shown in Fig. 8a. The received bit pattern stored temporarily in register 47 is compared to the wake-up bit pattern stored in register 22, and when both bit patterns are identically,
- 10 counter 51 of comparing means 21 is incremented in a step S6-2. The value of the counter 51 is compared in step S6-3 with a predetermined threshold value, and a wake-up command signal is generated in step S6-4 when the counter reaches the threshold value. The wake-up command signal switches the xDSL
- 15 modem 9 from the sleep mode to the operation mode.

List of reference numerals

	1	xDSL data transfer system
	2	data source
5	3	line
	4	xDSL user modem
	5	modem
	6	line
	7	wake-up signal generating circuit
10	8	telephone line
	9	xDSL modem
	10	central office
	11	modem
	12	wake-up signal detecting unit
15	13	line
	14	line
	15	cable
	16	bit pattern generating means
	17	line
20	18	half-length modulation circuit
	19	demodulating means
	20	line
	21	comparing means
	22	storing means
25	23	line
	24	line
	25	wake-up command generating means
	26	train sequencer
	27	rectifying means
30	28	low-pass filter
	29	comparing means
	30	operation amplifier
	31	resistors
	32	adjustable resistors
35	33	operational amplifier
	34	switches
	35	operational amplifier

36 resistor
37 resistor
38 resistor
39 capacitor
5 40 capacitor
41 line
42 operational amplifier
43 resistor
44 capacitor
10 43 synchronization means
44 pulse length detecting circuit
45 line
46 line
47 register
15 48 bit comparator
49 line
50 line
51 counter